PCT/DE98/00633   Sheet 1 of 1

Form P.T.O. 1449	U.S. Department of Commerce Patent and Trademark Office	Docket No. P99,0101	09/736777 New Application

LIST OF PRIOR ART CITED BY APPLICANT
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Applicant(s): PETER LIGGESMEYER
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U.S. PATENT DOCUMENTS

Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
	AA						
	AB						
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	AD						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation Yes	Translation No
CS	AE	EP 0 424 869 A1	02/05/91	Europe				
CS	AF	EP 0 352 759 B1	31/01/90	Europe				
CS	AG	EP 0 580 663 B1	02/02/94	Europe				
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CS	AI							

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

CS	AJ ✓	Burch, Jerry R. et al, "SYMBOLIC MODEL CHECKING FOR SEQUENTIAL CIRCUIT VERIFICATION", IEEE Transactions on Computer-Aided Design of Integrated Circuits and systems, Vol. 13, No. 4, April (1994), pp. 401-424
CS	AK ✓	Enders, Reinhard et al, "GENERATING BDDS FOR SYMBOLIC MODEL CHECKING IN CCS", Computer Aided Verification 3rd, July (1991), pp. 203-213
CS	AL ✓	DIN 25424, Part 1: Fehlerbaumanalyse: Methode und Bildzeichen; Part 2: Handrechenverfahren zur Auswertung eines Fehlerebaums
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CS	AN ✓	Nökel, K. et al, CONTROLLER SYNTHESIS AND VERIFICATION: A CASE STUDY, in: C. Leverentz, T. Lindner, Formal Development of Reactive Systems, Lecture Notes in Computer Science (No. 891), Springer 1995, pp. 55-74
CS	AO ✓	Bryant, R., SYMBOLIC BOOLEAN MANIPULATION WITH ORDERED BINARY-DECISION DIAGRAMS, ACM Computing surveys, Vol. 24, No. 3, September (1992), pp. 293-318
	AP	

Examiner	Date Considered 5/9/2002

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.